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Yves Quéré, Thierry Le Gougec, Pierre-Marie Martin, Denis Le Berre, Fabrice Huret. Frequency domain analysis of transmission zeroes on high-speed interconnects in the presence of an orthogonal metal grid underlayer. 11th IEEE Workshop on Signal Propagation on Interconnect (SPI07), Genova (Italy), May 2007, Genova, Italy. pp.95-98, 10.1109/SPI.2007.4512220 . hal-00492562

**HAL Id: hal-00492562**

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Submitted on 16 Jun 2010

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# Frequency domain analysis of transmission zeroes on high-speed interconnects in the presence of an orthogonal metal grid underlayer

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## Abstract

This paper addresses high-speed interconnects in high density systems (Systems on Chip (SoC) in package (SiP) ...). These lines (of microstrip or coplanar type) often have an underlayer of orthogonal metal grids which can affect transmission characteristics. We subsequently present a characterization through S-parameter measurements and electromagnetic simulations. Two kinds of grid are studied; grounded (*CC*) and floating grid (*CO*). In both cases, transmission zeroes appear. The position of these transmission zeroes in the frequency domain depends mainly on the grid length and, of course, on the grid charge *CC* or *CO*. In order to easily estimate it, we propose a simple equivalent circuit model which we validate by measurements and electromagnetic simulations. We then determine a set of expressions based on this model enabling us to analytically pinpoint the location transmission zero in the frequency domain, valid for any underlayer of orthogonal metal lines or grids.

## Introduction

Systems on chip (SoC) and systems in package (SiP) are currently undergoing a period of sustained development. For the SoC, these systems embed on the same chip, analogue functions, RF applications and numerical circuits in order to increase compactness and functionality. The various blocks are connected between them by interconnects realized in a complex 3D medium. With rising operating frequencies in silicon-based integrated circuits, the behavior of on-chip interconnects is becoming increasingly important in overall circuit performance. Transmission line effects need to be taken into account due to the long interconnect line lengths and high frequencies encountered [1]. Other concerns are interconnect line dispersion and losses [2]. Metal lines with simple geometrical configurations, such as single conductor lines, microstrip lines and coplanar waveguide (CPW) [3] have been the main vehicles for characterizing and modelling high-speed interconnect transmission line effects.

The conventional Manhattan routing practice [4] creates on-chip grids by placing two adjacent metal layers perpendicular to each other. It has been shown that the meshes have significant effects on the embedded transmission line characteristics [5][6]. Thus, the effects of orthogonal grids on the transmission characteristics of interconnect lines need to be characterized and modeled.

In this paper, we study the propagation on microstrip or coplanar-like interconnects placed on the higher levels also referred to as "global levels" and above an orthogonal metal

grid. We first present structures, measurements and a full-wave electromagnetic analysis of microstrip and coplanar lines situated above a metal grid. We highlight the appearance of a resonance frequency which corresponds to a transmission zero. We then propose a simple circuit model useful for resonant frequency prediction. This model is validated by comparison with measurements and full-wave electromagnetic analysis. Finally we propose analytical expressions allowing the prediction of transmission zero frequencies.

## Measurements and electromagnetic analysis.

Fig. 1 shows the microstrip and coplanar structures with grounded or floating grids respectively. The lines were implemented in multilayer technology available in our laboratory.

In the microstrip case, the lines of width  $W=30\mu\text{m}$  are placed over a substrate thickness  $H=60\mu\text{m}$  and permittivity  $\epsilon_R=4.3$ . The grids are located in the middle of the substrate and the lower part of the latter is a grounded plane. We have produced several grid densities, subsequently referred to as low density ( $W_G=30\mu\text{m}$ ,  $S_G=110\mu\text{m}$ ), medium density ( $W_G=30\mu\text{m}$ ,  $S_G=50\mu\text{m}$ ) and large density ( $W_G=90\mu\text{m}$ ,  $S_G=40\mu\text{m}$ ).

In the case of coplanar structures, there is no grounded plane, but a support in alumina ( $H_S=254\mu\text{m}$  and  $\epsilon_R=9.6$ ) under the first substrate. The dimensions of the CPW line are: width of ground  $W_M=270\mu\text{m}$ , space between ground and central conductor  $S=70\mu\text{m}$  and width of the central conductor  $W_C=30\mu\text{m}$ . The grids have the same characteristics as in the microstrip case.

In general,  $L_G$  denotes the length of the grid.

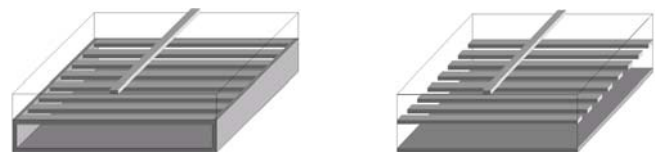


Fig. 1-a: microstrip lines

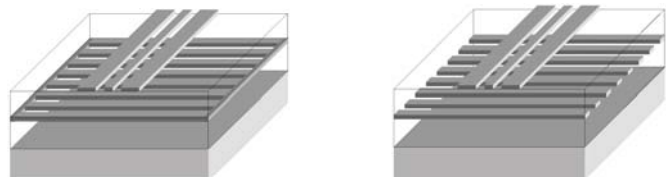
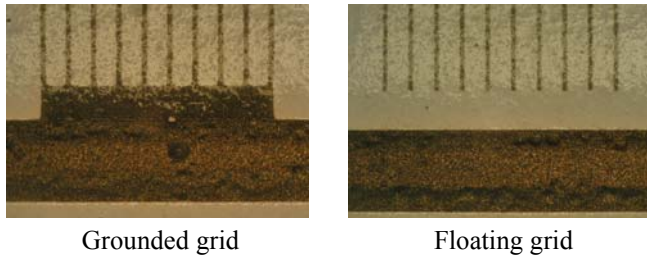


Fig 1-b: Coplanar lines

**Fig. 1. Realized microstrip and coplanar test structures**

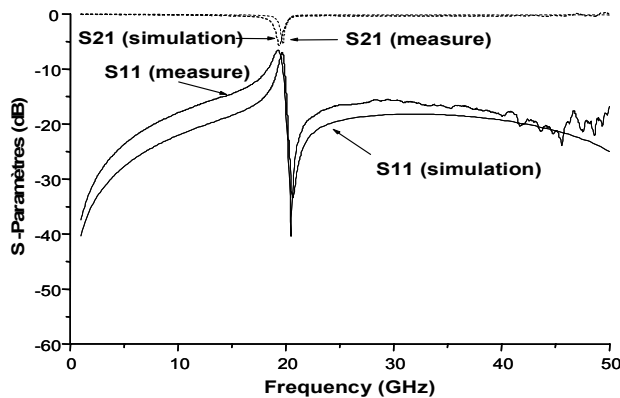
We present, some photographs of the connected and floating grids in Fig.2.



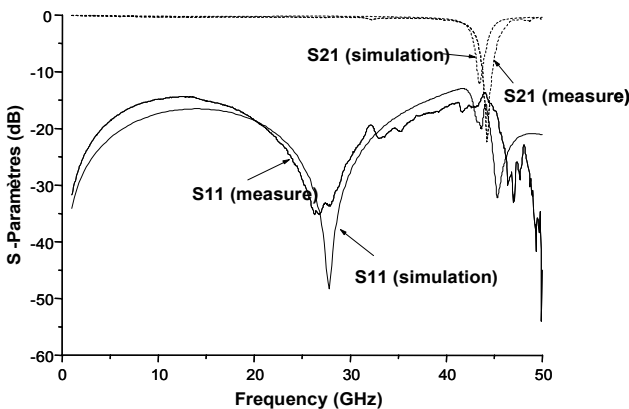
**Fig. 2. Photographs of the test structures.**

Several microstrip or CPW lines with no grid were also implemented on the same plates as the test structures to serve as references. All lines have been provided with coplanar access points allowing probe measurements. The measurements have been performed on a TEKTRONIC networks analyzer in the 1 to 50 GHz band after “TRL” (Thru Reflect Line) calibration.

We present the measured S-parameters of microstrip or coplanar lines in Fig. 3 and Fig. 4, with the orthogonal metal grid underlayer, grounded and, respectively, floating. The corresponding electromagnetic analysis performed with a fullwave Finit Element (FE) code (©Ansoft HFSS) is also presented on the figures.

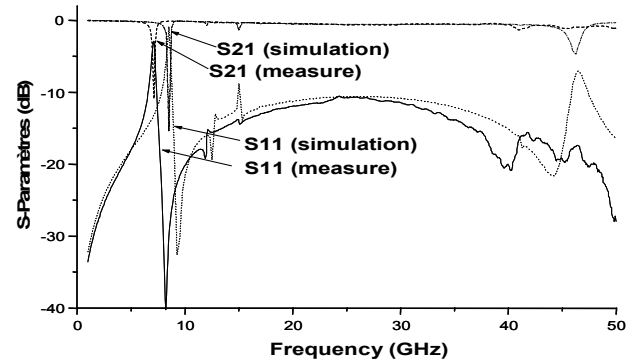


**Fig. 3a. S-parameters for microstrip line with grounded grid.**  
( $W_G=30\mu\text{m}$ ,  $S_G=50\mu\text{m}$ ,  $L_G=3285\mu\text{m}$ .)

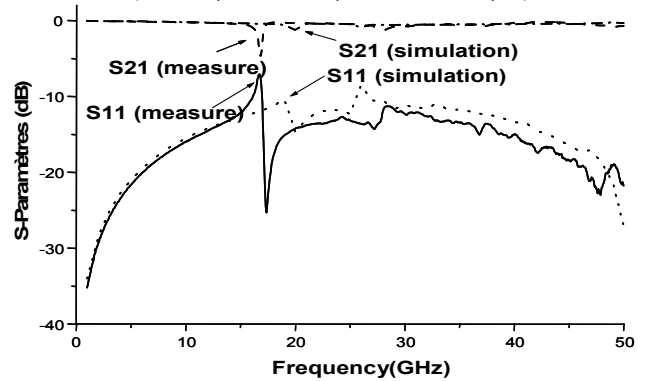


**Fig. 3b. S-parameters for microstrip line with floating grid.**  
( $W_G=30\mu\text{m}$ ,  $S_G=50\mu\text{m}$ ,  $L_G=3285\mu\text{m}$ )

**Fig. 3. Comparison of S-parameters measurement and FE simulations for microstrip structures**



**Fig. 4a. S-parameters for coplanar line with grounded grid.**  
( $W_G=30\mu\text{m}$ ,  $S_G=50\mu\text{m}$ ,  $L_G=3285\mu\text{m}$ )

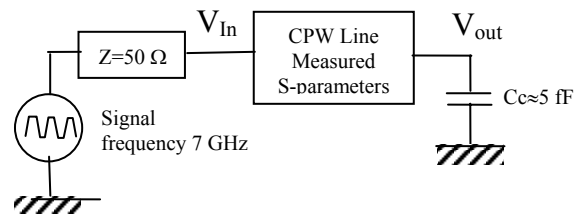


**Fig. 4b. S-parameters for coplanar line with floating grid.**  
( $W_G=30\mu\text{m}$ ,  $S_G=50\mu\text{m}$ ,  $L_G=3285\mu\text{m}$ )

**Fig. 4. Comparison of S-parameters measurement and FE simulations for coplanar structures.**

We first notice a good match between measurement and HFSS results, the small differences are probably due to uncertainties in the realization process. This validates the subsequent use of the full-wave electromagnetic simulator to study more structures and elaborate simple and efficient models of lines with orthogonal metal grid underlayer.

In all cases (microstrip or coplanar, with grounded or floating grid), a transmission zero appears for the S21 parameter in a certain frequency point. Models enabling the prediction of such transmission zeroes, that could prove very penalizing on signal propagation, need to be developed. To illustrate this, we have used the S-parameter measurement of the coplanar line with a connected grid underlayer, and we have made a time domain simulation. We present, the simulation principle in Fig. 5, the comparison of the S21 measured parameter and the input signal spectrum in Fig. 6, and the time domain simulation results in Fig. 7



**Fig. 5. Schematic's principle for time domain simulation.**

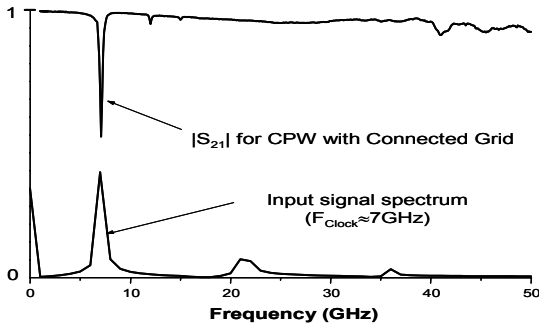


Fig. 6.  $|S_{21}|$  of CPW with connected grid and input signal spectrum.

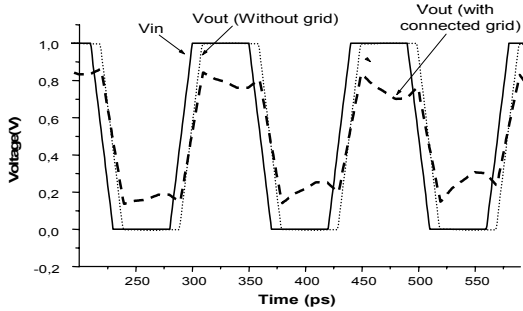


Fig. 7. Time domain results

We note that the location of the transmission zero is not very sensitive to grid density; it mainly seems to depend on the length  $L_G$  of the grid. In Fig. 8, we present the value of the resonance frequencies versus the length  $L_G$  for the microstrip line and with both grounded and floating grid respectively. These values are obtained through electromagnetic full-wave simulations (©Ansoft HFSS).

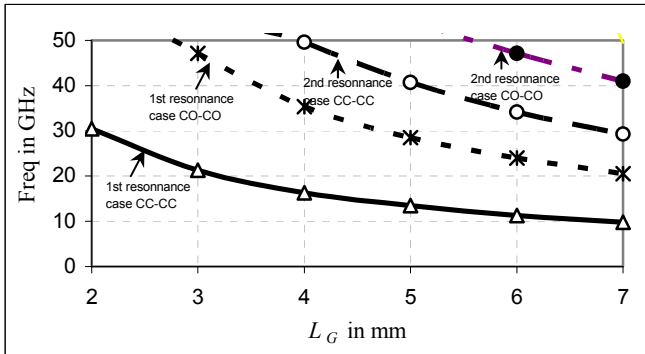


Fig. 8. Resonance frequency versus the length of the grid for the microstrip structure.

### Modelling and analysis.

Seeking to predict resonances frequencies, we sought to develop a simple circuit model describing the behavior of lines over a metal grid. As grids are homogenous, we can study, only one elementary cell at a first approach [7]. We present the concept of the basic cell for microstrip and coplanar structures in Fig. 9. To model a microstrip unit cell, we propose a general equivalent circuit using ideal transmission lines and lumped passive capacitance. The microstrip unit cell is modeled by a line of length  $L=S_G$ , with mid coupling to the grid line through a capacitance  $C_C$ . Two

impedances  $Z_{CH1}$  and  $Z_{CH2}$  form the grid line's extremities. It follows that  $Z_{CHi} \approx 0$  in the case of a connected and  $Z_{CHi} \approx \infty$  for the floating grid. The lengths of grid line on either side of the upper microstrip line can differ and they are subsequently denoted as  $L_{G1}$  and  $L_{G2}$ . This general unit microstrip cell model is presented Fig. 10.

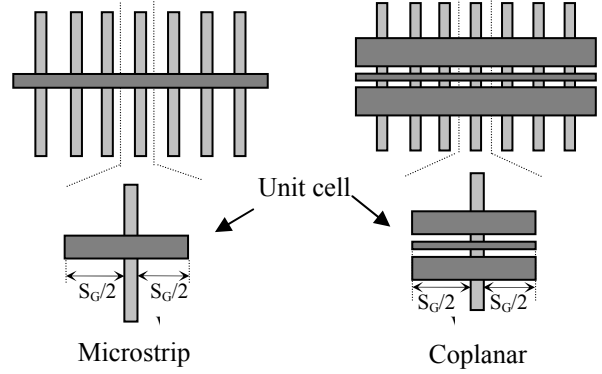


Fig. 9. Definition of a unit cell for microstrip with underlayer orthogonal metal grid

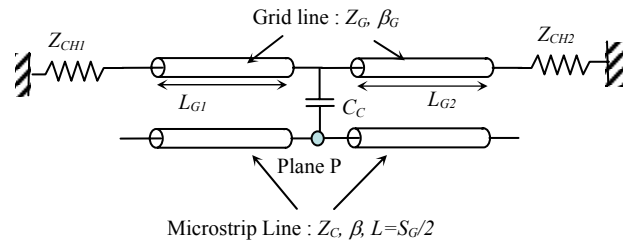


Fig. 10. Modelling of a microstrip unit cell

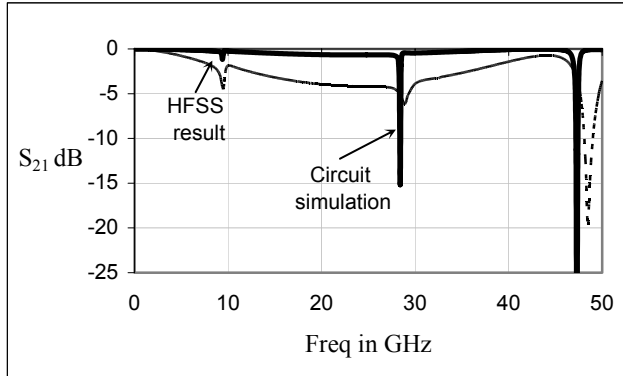
In figure 10,  $Z_C$  denotes the characteristic impedance of the microstrip line without a grid, and  $\beta$  the propagation factor.  $Z_G$  and  $\beta_G$  are respectively, the characteristic impedance and the propagation factor of the grid line. All these parameters can easily be determined. The capacitance  $C_C$  value can be evaluated by using the plane capacitance formula:

$$C_C = \epsilon_0 \epsilon_R \frac{W \cdot W_G}{H/2}. \quad (1)$$

$W$  and  $W_G$  respectively denote the widths of the upper microstrip line and of the grid line.  $H$  is the thickness of the substrate,  $\epsilon_R$  the relative permittivity and  $\epsilon_0$  the void permittivity.

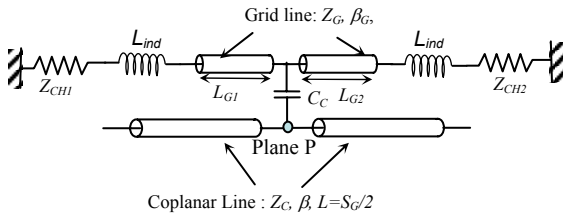
In Fig. 11 we present a comparison between the HFSS results and those obtained by circuit simulation using the unit cell previously described. This case corresponds to a microstrip line over a metal grid, grounded on one side and floating on the other. The grid dimensions are  $W_G=80 \mu\text{m}$ ,  $S_G=70 \mu\text{m}$  and the length  $L_G=3500 \mu\text{m}$ . We can see a good match between the resonance frequencies for the two approaches.

It should be noted that only resonance frequencies are of interest us, this is why we did not seek to otherwise improve our model.



**Fig. 11. Comparison between electromagnetic and circuit simulations in case of a grid grounded in one side and floating on the other ( $W_G=80\mu\text{m}$ ,  $S_G=70\mu\text{m}$ ,  $L_G=3500\mu\text{m}$ ).**

The coplanar model is slightly different from the microstrip one, it is presented Fig. 12. Two inductances  $L_{ind}$  have been introduced to simulate the part of the grid without the ground plane above. A more detailed presentation will be made available by the authors during the conference.



**Fig. 12. Modelling of a coplanar unit cell**

#### Computation of the microstrip resonance frequency.

The use of the simplified model enables us to determine the resonance frequencies which involve a transmission zero.

The impedance  $Z_P$  seen by the microstrip line in the plane P (see fig 7) can in a general case be expressed as:

$$Z_P = \frac{1}{jC_c \omega} + \frac{Z_{GC1} \cdot Z_{GC2}}{Z_{G1} + Z_{G2}} \quad (2)$$

where  $Z_{GC1}$  and  $Z_{GC2}$  are respectively the left and right impedances transferred by the grid line to the same point as the capacitance coupling  $C_c$ . These impedances are given by:

$$Z_{GCi} = Z_G \frac{Z_{CH(i)} + jZ_G \tan(\beta_G \cdot L_{G(i)})}{Z_G + jZ_{CH(i)} \tan(\beta_G \cdot L_{G(i)})} \quad (3)$$

where  $Z_G$  and  $\beta_G$  are the propagation characteristics of the grid line,  $Z_{CH(i)}$  denotes the impedances on the grid line extremities (for a connected grid ( $Z_{CHi} \approx 0$ ) and for a floating grid ( $Z_{CHi} \approx \infty$ )).  $L_{G(i)}$  is the length of the left or right part of the grid line.

If the microstrip line is short-circuited in plane P, a transmission zero will occur. Consequently, the frequencies where transmission zeros appear are the solutions to equation 4.

$$Z_P = 0. \quad (4)$$

In the case where both sides of the grid are connected to the ground the two  $Z_{CH(i)}$  impedances are negligible ( $Z_{CH(i)} \approx 0$ ) and the expression of the  $Z_P$  impedance therefore becomes:

$$Z_P = \frac{-j}{C_c \omega} + jZ_G \frac{\tan(\beta_G L_{G1}) \tan(\beta_{Grid} L_{G2})}{\tan(\beta_G L_{G1}) + \tan(\beta_G L_{G2})}. \quad (5)$$

In the case of the floating grid the two load impedances tend to infinity ( $Z_{CH(i)} \approx \infty$ ) and then the  $Z_P$  impedance can be written as:

$$Z_P = \frac{-j}{C_c \omega} + jZ_G \frac{1}{\tan(\beta_G L_{G1}) + \tan(\beta_G L_{G2})}. \quad (6)$$

In the more general case of a grid grounded on one side and floating on the other, the impedance in plane P can be calculated by:

$$Z_P = \frac{-j}{C_c \omega} + jZ_G \frac{\tan(\beta_G L_{G1})}{1 - \tan(\beta_G L_{G1}) \cdot \tan(\beta_G L_{G2})} \quad (7)$$

#### Conclusions

In this paper, we highlight the appearance of transmission zeros for lines situated above an orthogonal metal grid. These transmission zeros can be very penalizing on signal propagation. We consequently propose a simple circuit model to predict the frequencies involving zeros. This simple model based on a circuit approach can be very interesting for VLSI circuit designers. We also translated the circuit model into equations to avoid simulations and to enable rapid analytical computation of orthogonal metal grid critical lengths.

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